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	Substitu	te for f	 orm 1449B/PTO	Complete if Known		
				Application Number	10/721,879	
INFORMATION DISCLOSURE				Filing Date	November 26, 2003	
STATEMENT BY APPLICANT (use as many sheets as necessary)			BY APPLICANT	First Named Inventor	DEVOR, Harold Theodore	
				Group Art Unit	2183	
			eets as necessary)	Examiner Name	Not yet known	
Sheet	1	of	1	Attorney Docket Number	P-6216-US	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
А	A	Hookway, R., and Herdeg, Mark A., "Digital FX!32: Combining Emulation and	
RF.		Binary Translation". Digital Technical Journal, Vol. 9, No. 1, 1997. Pages 3-12.	
	В	Baraz, et al., "IA-32 Execution Layer: a two-phase dynamic translator designed	
D.		to support IA-32 applications on Itanium®-based systems". Proceedings of the	
		36 th International Symposium on Microartichecture (MICRO-36 2003).	
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Examiner Signature	Date Considered	2/13/06

[•] EXAMINER: tritial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered include copy of this form with next communication to applicant

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Filing Date		November 2	26, 2003
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Examiner Initials*	Cite No. '	Number	Kind Code (if known)	Name of F	Patentee or Applicant ited Document	Cited	Publication of Document -DD-YYYY	Where Relevant A Passages or Relevant Figures Appear
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